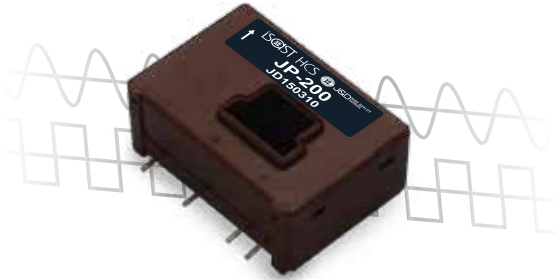


# CLOSED-LOOP CURRENT SENSOR JP-200

PCB  
Mounting



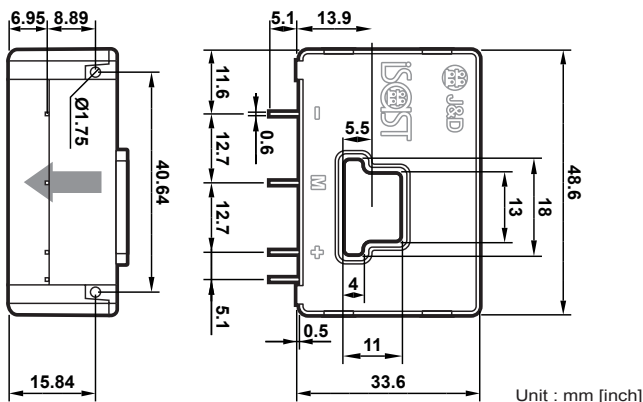
## >>> Features

- Closed loop (compensated) current transducer using the Hall effect
- Printed circuit board mounting
- Insulated plastic case recognized according to UL 94-V0.

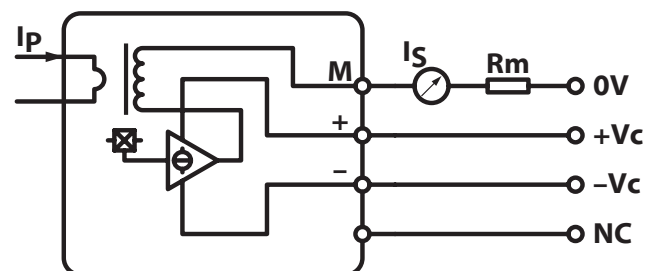
## >>> Electrical Properties

Model	JP-200	
Primary nominal current	$I_f$	200A
Measuring resistance	$R_L$	$V_{CC}=\pm 12V @ \pm 200A : 0\Omega \sim 26\Omega$ $V_{CC}=\pm 12V @ \pm 250A : 0\Omega \sim 4\Omega$ $V_{CC}=\pm 15V @ \pm 200A : 0\Omega \sim 56\Omega$ $V_{CC}=\pm 15V @ \pm 300A : 0\Omega \sim 8\Omega$
Rated output current	$I_o$	100mA (Turn ratio 1 : 2000)
Output current accuracy	$V$	$\pm 0.4 (\pm 15V, +25^\circ C)$
Offset current	$I_{of}$	$\leq \pm 0.2mA$ (at $I_f=0A$ )
Output linearity	$\epsilon_L$	$\leq \pm 0.15%$ (at $I_f$ )
Power supply voltage	$V_{CC}$	$\pm 12V \pm 5\% \sim \pm 15V \pm 5\%$ (Rated output current is restricted by $V_{CC}$ )
Response time	$t_r$	$\leq 1\mu S$ (at $di/dt=I_f/\mu s$ )
Frequency characteristics	$f$	DC...100kHz (-1 dB)
Thermal drift of gain	$TCl_o$	$\leq \pm 0.01\%/^\circ C$ (Without $Tcl_{of}$ )
Thermal drift of offset	$TCl_{of}$	$\leq \pm 0.5mA$
Hysteresis error	$I_{oH}$	$\leq 0.3mA$ (at $I_f=0A \rightarrow I_f \rightarrow I_f=0A$ )
Insulation voltage	$V_D$	AC3000V for 1 minute (Sensing current 0.5mA) inside of through hole $\leftrightarrow$ terminal
Insulation resistance	$R_{is}$	$\geq 500M\Omega$ (at DC500V) inside of through hole $\leftrightarrow$ terminal
Ambient Operating temperature	$T_A$	$-40^\circ C \sim +85^\circ C$
Ambient storage temperature	$T_S$	$-40^\circ C \sim +90^\circ C$
Secondary coil resistance	$R_s$	$76\Omega$ (@ $T_a=70^\circ C$ ) $80\Omega$ (@ $T_a=85^\circ C$ )

## >>> Dimension



## >>> Connection



Unless otherwise specified,  
tolerances shall be  $\pm 0.5mm$